



Teaching Guide				
Identifying Data			2019/20	
Subject (*)	Electricity and Electronics		Code	631G01206
Study programme	Grao en Náutica e Transporte Marítimo			
Descriptors				
Cycle	Period	Year	Type	Credits
Graduate	1st four-month period	Second	Obligatory	6
Language	SpanishGalician			
Teaching method	Face-to-face			
Prerequisites				
Department	Enxeñaría de Computadores			
Coordinador	Novo Vidal, Maria Elena		E-mail	e.novo@udc.es
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General description	Nesta materia búscase que o alumno adquira os coñecementos básicos de compoñentes e circuitos que conforman os sistemas eléctricos e electrónicos do buque. Devanditos coñecementos permitiránlle avaliar o funcionamento dos sistemas de potencia, control y comunicacións do barco, ademais de adquirir xuízo crítico para detectar fallos e resolvelos.			

Study programme competences	
Code	Study programme competences
A6	Localizar avarías sistemáticamente nun equipo electrónico.
A8	Modelizar situacións e resolver problemas con técnicas ou ferramentas físico-matemáticas.
A9	Avaliación cualitativa e cuantitativa de datos e resultados, así como representación e interpretación matemática de resultados obtidos experimentalmente.
A10	Redactar e interpretar documentación técnica e publicacións náuticas.
B2	Resolver problemas de xeito efectivo.
B5	Traballar de forma autónoma con iniciativa.
B6	Traballar de forma colaboradora.
B8	Aprender en ámbitos de teleformación.
B10	Versatilidade.
B11	Capacidade de adaptación a novas situacións.
B12	Uso das novas tecnoloxías TIC, e de Internet como medio de comunicación e como fonte de información.
B13	Comunicar por escrito e oralmente os coñecementos procedentes da linguaxe científica.
B14	Capacidade de análise e síntese.
B15	Capacidade para adquirir e aplicar coñecementos.
B16	Organizar, planificar e resolver problemas.
B19	Utilizar as ferramentas básicas das tecnoloxías da información e as comunicacións (TIC) necesarias para o exercicio da súa profesión e para a aprendizaxe ao longo da súa vida.
B22	Valorar criticamente o coñecemento, a tecnoloxía e a información dispoñible para resolver os problemas cos que deben enfrontarse.
C10	Que os estudantes saiban aplicar os coñecementos adquiridos e a súa capacidade de resolución de problemas en contornas novas ou pouco coñecidas dentro de contextos máis amplas (ou multidisciplinares) relacionados coa súa área de estudo
C13	Que os estudantes posúan as habilidades de aprendizaxe que lles permitan continuar estudando dun modo que haberá de ser en grande medida autodirixido ou autónomo.

Learning outcomes	
Learning outcomes	Study programme competences



Be able to interpret electrical diagrams.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	
Be able to analyze electrical installations.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	
Practical applications of analog and digital integrated circuits, and solid state devices.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	



Know the electrical alternators.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	
Evaluate powers.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	
Know the operation of electronic instrumentation.	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	



Knowledge of the characteristics of basic semiconductor devices	A6	B2	C10
	A8	B5	C13
	A9	B6	
	A10	B8	
		B10	
		B11	
		B12	
		B13	
		B14	
		B15	
		B16	
		B19	
		B22	

Contents	
Topic	Sub-topic
THEME 1: INTRODUCTION AND CONCEPTS OF CIRCUITS.	1.1. Electrical magnitudes and units. 1.2. Force, work and power. 1.3. Charge and electric current. 1.4. Electric potential. 1.5. Conductors and insulators. 1.6. Electric energy and power. 1.7. Active and passive elements. 1.8. Ohm's law. 1.9. Resistors. Capacitors. Inductances. 1.10. Sources of voltage. 1.11. Circuit diagrams. 1.12. Measuring instruments.
THEME 2: LAWS OF CIRCUITS AND METHODS OF ANALYSIS.	2.1. Kirchhoff's laws. 2.2. Elements in series and in parallel. 2.3. Division of tension and division of current. 2.4. Superposition theorem. 2.5. Theorems of Thévenin and Norton. 2.6. Maximum power transfer theorem. 2.7. Circuit analysis.
THEME 3: ANALYSIS OF SINUSOIDAL CIRCUITS.	3.1. Periodic functions. Sinusoidal functions. 3.2. Average and effective values. 3.3. Response of the elements R, L, C. 3.4. Phasors. 3.5. Impedance and admittance. 3.6. Power in permanent sinusoidal regime. Active power. Reactive power. Apparent power. Triangle of powers. 3.7. Phasorial Kirchhoff's laws. 3.8. Division of voltage and current. 3.9. Theorems of Thévenin and Norton. 3.10. Circuit analysis. 3.11. Transformers.



THEME 4: FOUNDATIONS OF ENERGY DISTRIBUTION.	4.1. Fundamentals of three-phase systems. 4.2. Basic elements of protection of facilities. 4.3. General fundamentals of alternators. 4.4. General principles of electric motors. 4.5. Electrical installations. Examples of electrical drawings.
THEME 5. SEMICONDUCTORS. THE DIODE.	5.1. Semiconductors. 5.2. The PN junction. 5.3. V-I characteristic of a diode. 5.6. Types of diodes.
THEME 6. CIRCUITS WITH DIODES: RECTIFIERS.	6.1. Analysis of circuits with diodes. 6.2. Rectifiers.
THEME 7. THE BIPOLAR TRANSISTOR. CIRCUITS WITH BJT TRANSISTORS.	7.1. The Bipolar Transistor. 7.2. The V-I characteristics. 7.3. Analysis of circuits with BJT.
THEME 8. UNIPOLAR TRANSISTOR. UNIPOLAR TRANSISTOR CIRCUITS.	8.1. Field-effect transistors. 8.2. The field-effect transistors V-I characteristics. 8.3. Analysis of circuits with field-effect transistors.
THEME 9. GENERAL CONCEPTS OF AMPLIFIERS. THE OPERATIONAL AMPLIFIER.	9.1. Basics of amplification. 9.2. The Operational Amplifier. 9.3. Analysis of circuits.
THEME 10. LOGIC GATES. APPLICATIONS.	10.1. Digital circuits. 10.2. Logic gates. 10.3. Logic families. 10.4. Analysis of circuits.
PROBLEM SOLVING SESSIONS.	SESSION 1: Introduction and concepts of circuits. SESSION 2: Laws of circuits and methods of analysis. SESSION 3: Analysis of sinusoidal circuits. SESSION 4: Analysis of sinusoidal circuits. SESSION 5: Resolution of problems of fundamentals of energy distribution. SESSION 6: Analysis of circuits with Diodes and Rectifiers. SESSION 7: Analysis of circuits with Bipolar Transistors. SESSION 8: Analysis of circuits with Bipolar Transistors. SESSION 9: Analysis of circuits with Unipolar Transistors. SESSION 10: Analysis of circuits with Operational Amplifiers.
LABORATORY PRACTICES.	PRACTICE 1: EQUIPMENT HANDLING (I). 1.1. Feeding source and multimeter. 1.2. Measurement of resistances. 1.3. Measurement of DC voltages and currents with multimeter. PRACTICE 2: EQUIPMENT HANDLING (II). 2.1. Signals generator and oscilloscope. 2.2. Measurement of AC voltages with multimeter and oscilloscope.
ITC PRACTICALS.	Circuit design and measurement practices will be carried out with the LTSpice software according to the theory syllabus.



O desenvolvemento e superación destes contidos, xunto cos correspondentes a outras materias que inclúan a adquisición de competencias específicas da titulación, garanten o coñecemento, comprensión e suficiencia das competencias recollidas no cadro AII/2, do Convenio STCW, relacionadas co nivel de xestión de Primeiro Oficial de Ponte da Mariña Mercante, sen limitación de arqueo bruto e Capitán da Mariña Mercante ata o máximo de 3000 GT. Cadro A-II/2 do Convenio STCW.	Especificación das normas mínimas de competencia aplicables a Capitáns e primeiros oficiais de ponte de buques de arqueo bruto igual ou superior a 500 GT.
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Planning				
Methodologies / tests	Competencies	Ordinary class hours	Student?s personal work hours	Total hours
Guest lecture / keynote speech	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	30	45	75
Laboratory practice	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	10	10	20
ICT practicals	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	10	10	20
Speaking test	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	0.25	0.75	1
Problem solving	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	10	20	30
Mixed objective/subjective test	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	3	0	3
Personalized attention		1	0	1
(*)The information in the planning table is for guidance only and does not take into account the heterogeneity of the students.				

Methodologies	
Methodologies	Description
Guest lecture / keynote speech	Didactic exposition, using slides and blackboard of the theoretical content of the subject.
Laboratory practice	Students will work on a series of practices in the Electronics Laboratory working with an electronic practice board and the available measurement materials. The students will have to answer corresponding sets of questions related to the themes to be developed in each practice.
ICT practicals	Students will work on a series of practices on a PC using the electronic circuits simulator LTspice. The students will have to answer corresponding sets of questions related to the themes to be developed in each practical.



Speaking test	Short answer objective test to evaluate the knowledge and skills acquired by students in the management of electronic instrumentation during laboratory practices.
Problem solving	Approach and resolution of problems related to the contents of the subject.
Mixed objective/subjective test	Mixed exam written by the theory Professor about the contents of the course.

Personalized attention	
Methodologies	Description
Laboratory practice	Keynote session: assist and answer questions from the students in relation to the theoretical material exposed in the keynote sessions.
Guest lecture / keynote speech	
ICT practicals	Problems solving: addressing and solving concerns of students in relation to the problems solved or posed by the teacher in the problem solving sessions.
Problem solving	
Speaking test	Laboratory practice: attend and answer questions from students in relation to practices posed or carried out in the laboratory.
	Practices through ICT: addressing and solving concerns of students in relation to ICT practices posed or carried out.
	Personal attention: in relation to classes of theory and problem solving sessions, preferably use tutoring hours individually.
	In relation to practical classes, hours of tutoring will be used preferably individually, although e-mail will be possible.

Assessment			
Methodologies	Competencies	Description	Qualification
Laboratory practice	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	Attending the practices and performing their corresponding projects will be deemed positive. At the beginning of each practice, the student will have to write the answers to a set of three (3) brief questions (tests) related to the above mentioned practice. If the student does not answer correctly at least two of those questions, the score obtained in the practice will be halved. The students registered part-time or with academic dispensation of attendance exemption will have the option of taking an exam regarding laboratory practices at the end of the course.	3.75
Guest lecture / keynote speech	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	Answers to the teacher during the course in the keynote sessions could be estimated positively. Works carried out independently by the student and posed by the Professor of theory can be presented optionally.	0
ICT practicals	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	Attending the practices and performing their corresponding projects will be deemed positive. At the beginning of each practice, the student will have to write the answers to a set of three (3) brief questions (tests) related to the abovementioned practice. If the student does not answer correctly at least two of those questions, the score obtained in the practice will be halved. The students registered part-time or with academic dispensation of attendance exemption will have the option of taking an exam regarding ITC practices at the end of the course.	3.75



Mixed objective/subjective test	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	It will consist of a theory and problem-solving exam on the contents exposed throughout the course during the lectures and problem-solving sessions, evaluating the comprehension of said contents and their application in solving problems. Works carried out independently by the student and posed by the Professor of theory can be presented optionally. The participation of the student in the exercises or works posed by the teacher during the course in the keynote and problem-solving sessions and in the tutorials could be estimated positively. To pass the course 4.5 out of 9 must be reached in the final marks of theory and problems.	90
Problem solving	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	Answers to the teacher during the course in the keynote and problem-solving sessions could be estimated positively.	0
Speaking test	A6 A8 A9 A10 B2 B5 B6 B8 B10 B11 B12 B13 B14 B15 B16 B19 B22 C10 C13	At the date established by the professor, an evaluation of the defense of a supervised project of practices will be performed in an oral session. Mainly will be evaluated the clarity when presenting the results and the critical analysis of them.	2.5
Others			

Assessment comments

The evaluation of the content taught in masterclasses and problem solving sessions of the subject represents 90% of the total marks. The evaluation of laboratory and ICT practices by the short answer questions is the remaining 10%.

To pass the course will be required:

1)

Mixed exam: written exam about the content taught in masterclasses and problem solving sessions: have a minimum of 4 points out of 9. The student must demonstrate a basic knowledge of all the content of the subject in this exam. To pass the exam none of the answers to the questions can be blank.

Works carried out independently by the student and posed by the Professor of theory can be presented optionally.

Answers to the teacher during the course in the keynote and problem-solving sessions and in the tutorials could be estimated positively.

To pass the course, 4.5 out of 9 must be reached in the final marks of theory and problems.

2) Practices: To have a minimum of 0.5 points in the practices.

Attending the practices and performing their corresponding projects will be assessed positively. At the beginning of each practice, the student will have to write the answers to a set of three (3) brief questions (tests) related to the abovementioned practice. If the student does not answer correctly at least two of those questions, the score obtained in the practice will be halved.

The students registered part-time or with academic dispensation of attendance exemption will have the option of taking an exam about laboratory/ITC practices at the end of the course.

If a minimum of 4.5 out of 9 is not obtained in the final marks of theory and problems, the practice marks will be divided by two to calculate the final marks. If in practice the minimum 0.5 was not obtained, to calculate the final grade the geometric mean weighted will be made.

The evaluation criteria considered in table A-II/1 of the STCW Code and its amendments related to this subject shall be taken into account when designing and evaluating.

Sources of information



Basic	- R. L. Boylestad y L. Nashelsky (2009). Electrónica: teoría de circuitos y dispositivos electrónicos. Ed. Prentice Hall (10ª Edición) - R. L. Boylestad (). Introducción al análisis de circuitos. Ed. Prentice Hall - Mª Elena Novo Vidal (2019). Copia de las diapositivas de la asignatura con problemas resueltos. Reprografía - J.A.Edminister (). Circuitos eléctricos . Ed. McGraw Hill (Serie Schaum). - Jacob Millman y Christos C. Halkias. (). Electrónica integrada: Circuitos y Sistemas Analógicos y Digitales. Editorial Hispano-Europea.- (6ª Edición). - J.A.Edminister y Mahmood Nahvi (). Circuitos eléctricos. Ed. McGraw Hill (Serie Schaum).
Complementary	- Albert Malvino y David J. Bates (2.010.). Principios de electrónica. Mac Graw Hill. (7ª Edición). - Jacob Millman y Arvin Grabel. (). Microelectrónica. Editorial Hispano-Europea.(6ª edición). - Jacob Millman. (). Microelectrónica: Circuitos y Sistemas Analógicos y Digitales. Editorial Hispano-Europea. (3ª edición). - Jacob Millman y Christos C. Halkias (). Dispositivos y circuitos electrónicos. Editorial Pirámide. 10ª Edición. - Linear Technology (2008). LTSpice User's Guide. Linear Technology - Keysight Technologies (2012). Osciloscopios de la serie 1000B de Keysight. Guía del usuario. Keysight Technologies - Julio Brégains (2016). Tutoriales de medidas de circuitos eléctricos y electrónicos. Plataforma moodle (moodle.udc.es)

Recommendations

Subjects that it is recommended to have taken before

Mathematics I/631G01101

Physics/631G01103

Mathematics II/631G01106

Subjects that are recommended to be taken simultaneously

Subjects that continue the syllabus

Ship's Energy and auxiliary systems/631G01204

Maritime Radiocommunications/631G01307

Navigation and communications systems/631G01311

Other comments

(*)The teaching guide is the document in which the URV publishes the information about all its courses. It is a public document and cannot be modified. Only in exceptional cases can it be revised by the competent agent or duly revised so that it is in line with current legislation.